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**LATTICE SEMICONDUCTOR CORPORATION
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<i>MARK BECK</i>	(Depositor's name)
<i>Mark Beck</i>	(Signature)
2/10/04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/626,089	07/24/2003	Chun Jiang	L01-029D1	4530

TITLE OF INVENTION: ZERO POWER MEMORY CELL WITH REDUCED THRESHOLD VOLTAGE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	04/21/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TRAN, MAI HUONG C	2818	257-315000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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(A) NAME OF ASSIGNEE

Lattice Semiconductor Corporation

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent):

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